

AMENDMENTS TO THE CLAIMS

(IN FORMAT COMPLIANT WITH THE REVISED 37 CFR 1.121)

Please cancel claims 5, 14 and 15 without prejudice.

1. (CURRENTLY AMENDED) The apparatus comprising:

a first circuit configured to present a first portion of an output data stream in response to a first portion of an input data stream; and

5 a second circuit configured to present a second portion of said output data stream in response to a second portion of said input data stream, wherein (i) said apparatus is configured to perform color and gamma correction on said input data stream to generate said output data stream in response to one or more control
10 signals and (ii) said second circuit comprises (a) a color corrector circuit configured to generate a first intermediate signal in response to said second portion of said input data stream, (b) a gamma corrector circuit configured to generate a second intermediate signal in response to said first intermediate
15 signal, and (c) a multiplexer configured to present either said first intermediate signal or said second intermediate signal in response to said control signals.

1 2. (ORIGINAL) The apparatus according to claim 1, wherein said apparatus comprises a block move engine (BME).

3. (ORIGINAL) The apparatus according to claim 1, wherein said first circuit comprises a delay circuit.

4. (ORIGINAL) The apparatus according to claim 1, wherein said second circuit comprises a correction circuit.

5. (CANCELED)

6. (ORIGINAL) The apparatus according to claim 1, wherein said control signals comprise:

one or more coefficient signals.

7. (ORIGINAL) The apparatus according to claim 1, wherein said control signals comprise:

one or more offset signals.

8. (ORIGINAL) The apparatus according to claim 1, wherein said control signal comprises:

one or more enable signals.

9. (ORIGINAL) The apparatus according to claim 1, wherein said input data stream comprises video and graphics data.

10. (ORIGINAL) The apparatus according to claim 2, wherein said BME comprises a block modify and move engine (BMME).

11. (ORIGINAL) The apparatus according to claim 2, wherein said BME is further configured to perform color and gamma conversion.

12. (CURRENTLY AMENDED) An apparatus comprising:

means for generating a first portion of an output data stream in response to a first portion of an input data stream; and

means for generating a second portion of said output data

5 stream in response to a second portion of said input data stream,

wherein (i) said apparatus is configured to perform color and gamma correction on said input data stream to generate said output data

stream in response to one or more control signals and means for generating said second portion comprises (a) a color corrector

10 circuit configured to generate a first intermediate signal in

response to said second portion of said input data stream, (b) a

gamma corrector circuit configured to generate a second intermediate signal in response to said first intermediate signal,

and (c) a multiplexer configured to present either said first

15 intermediate signal or said second intermediate signal in response to said control signals.

13. (CURRENTLY AMENDED) A method for providing color and gamma conversion, comprising the steps of:

(A) generating a first portion of an output data stream ~~in response to~~ by delaying a first portion of an input data stream,
5 wherein step (A) further comprises (i) color correcting a second portion of said input data stream, (ii) gamma correcting said second portion of said input data stream, and (iii) bypassing said gamma correcting step; and

(B) generating a second portion of said output data
10 stream in response to a second portion of said input data stream, wherein said method performs color and gamma correction on said input data stream to generate said output data stream in response to one or more control signals.

14. (CANCELED)

15. (CANCELED)

16. (ORIGINAL) The method according to claim 13, wherein said control signals comprise:

one or more coefficient signals.

17. (ORIGINAL) The method according to claim 13, wherein said control signals comprise:

one or more offset signals.

18. (ORIGINAL) The method according to claim 13, wherein said control signal comprises:

one or more enable signals.

19. (ORIGINAL) The method according to claim 13, wherein said input data stream comprises video and graphics data.

20. (ORIGINAL) A block modify and move engine (BMME) configured to perform the steps of claim 13.